

FIG. 1 PRIOR ART

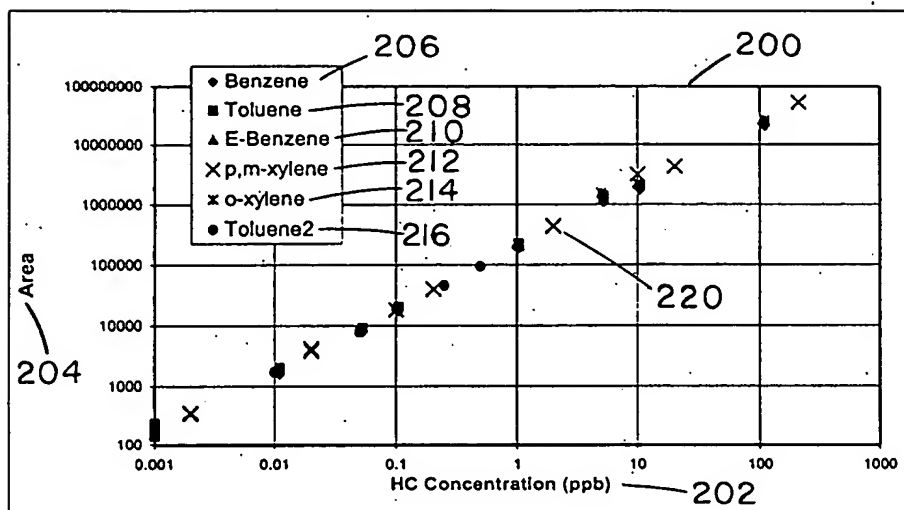


FIG. 2 PRIOR ART

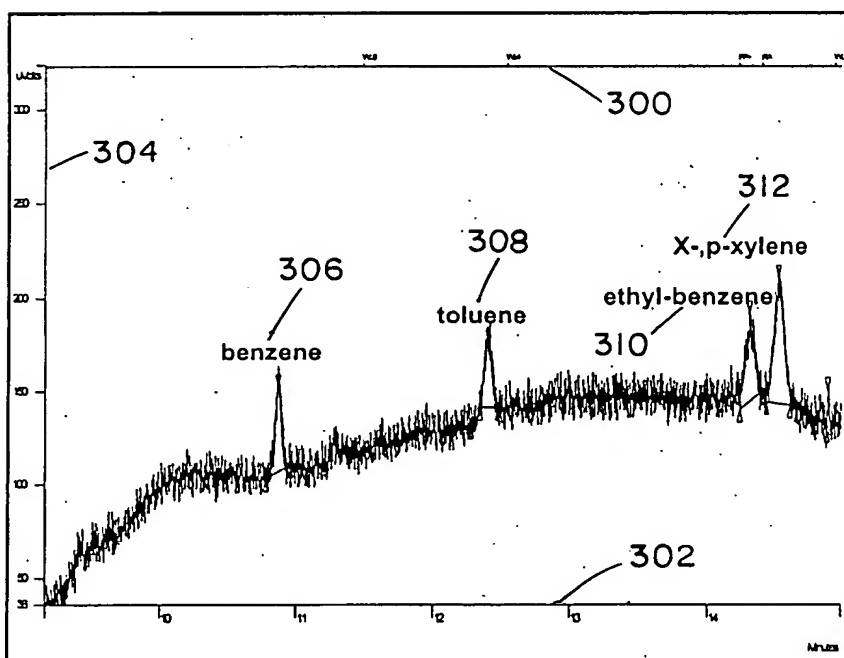


FIG. 3 PRIOR ART

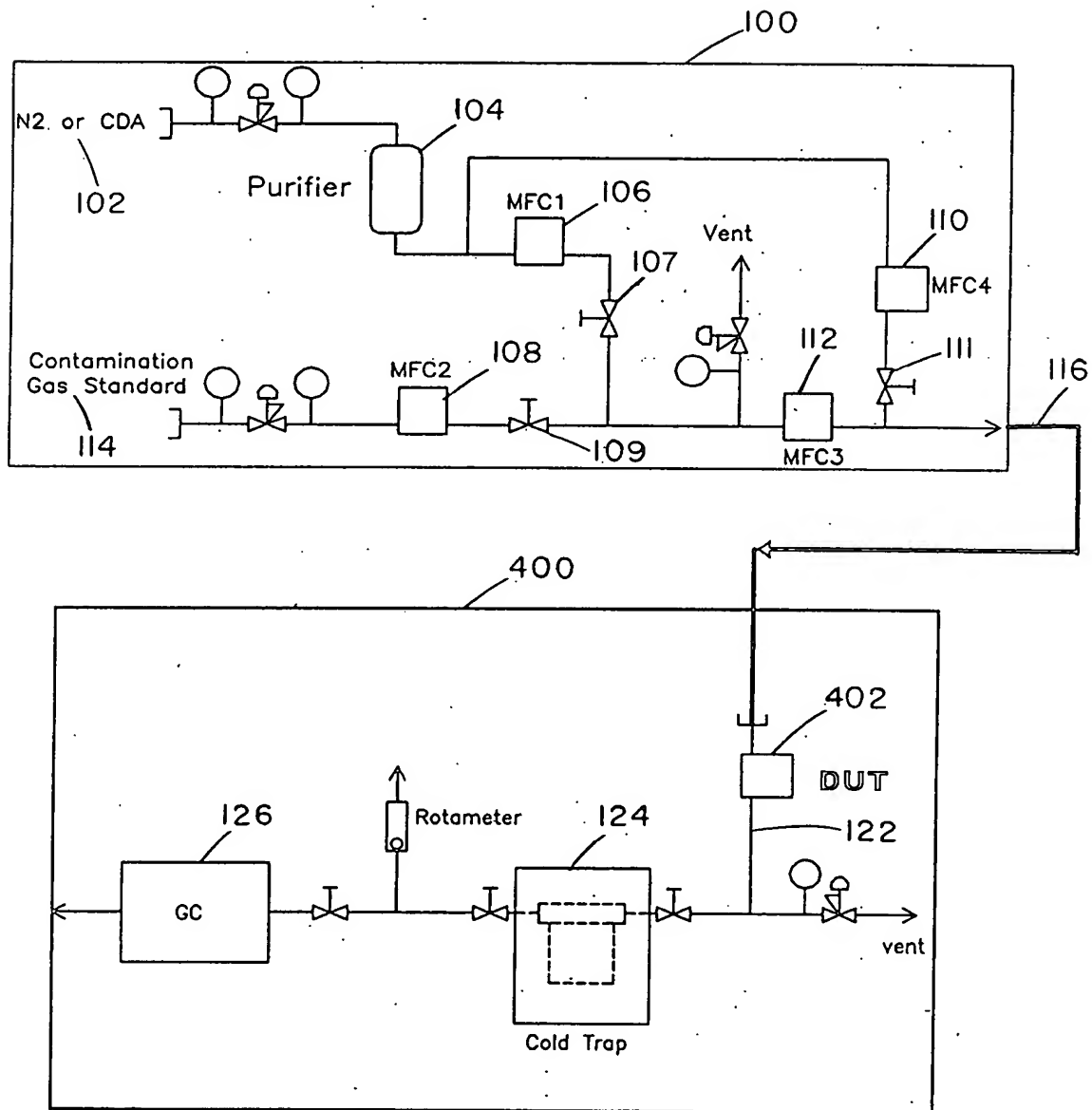
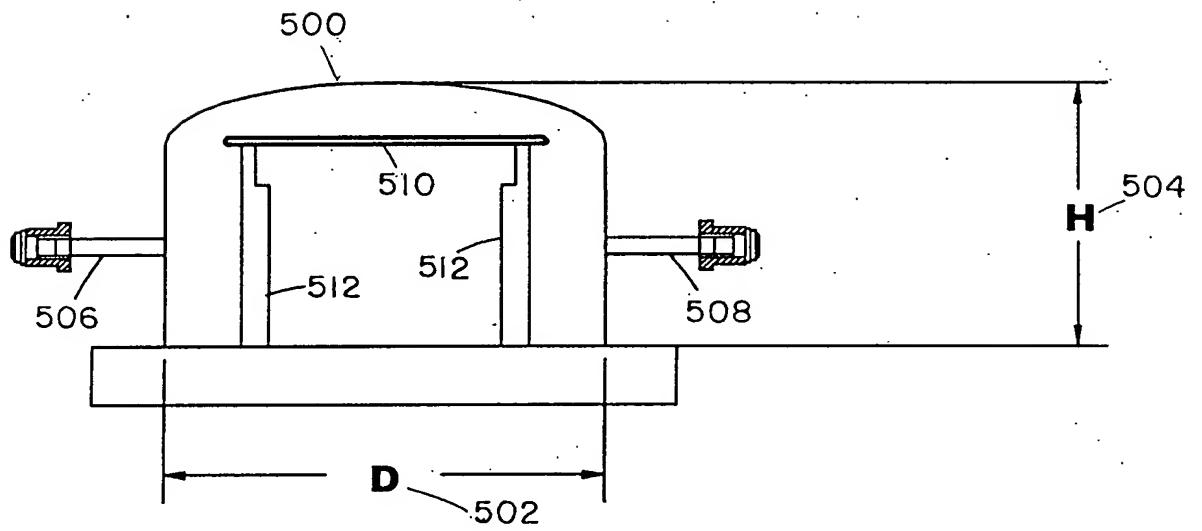


FIG. 4



Wafer Chamber

FIG. 5

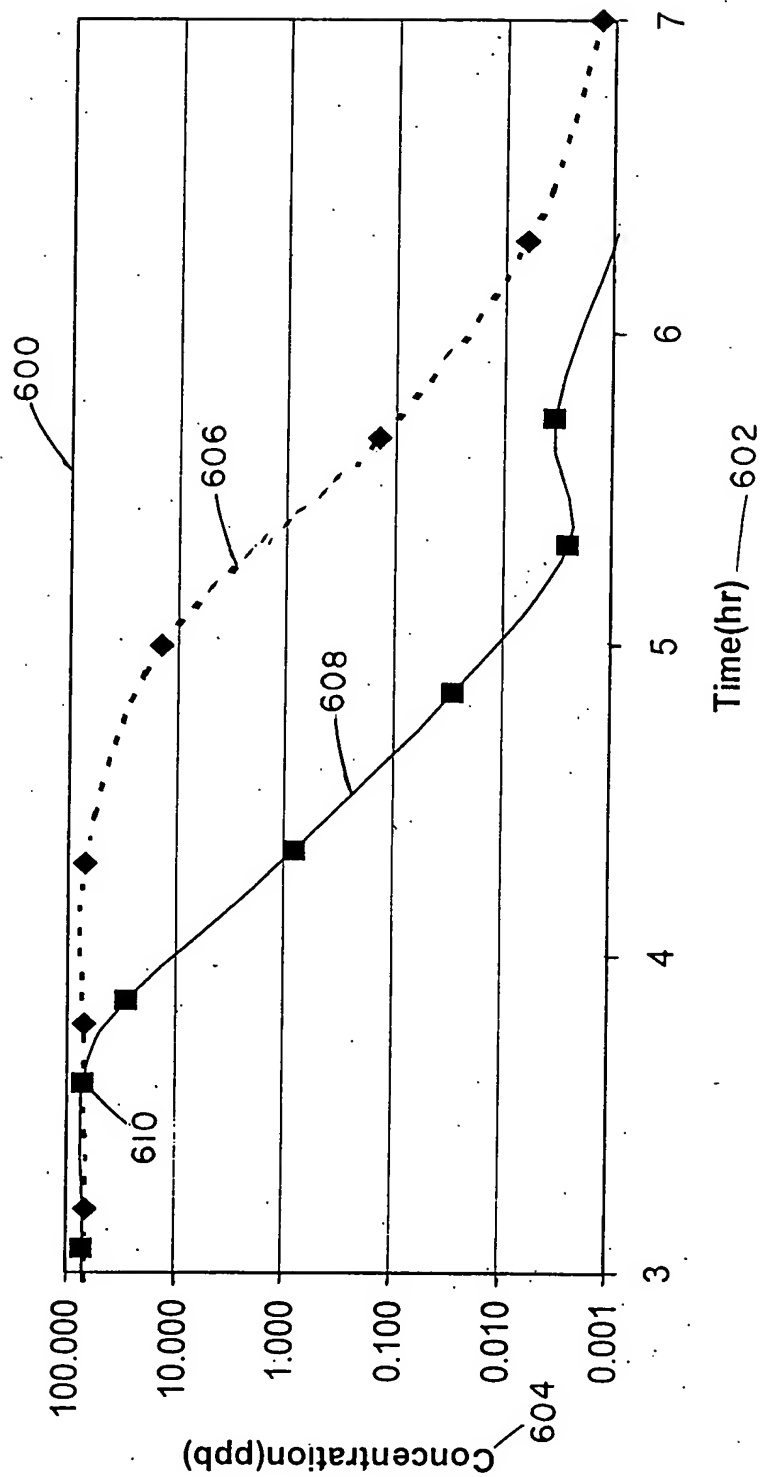


FIG. 6

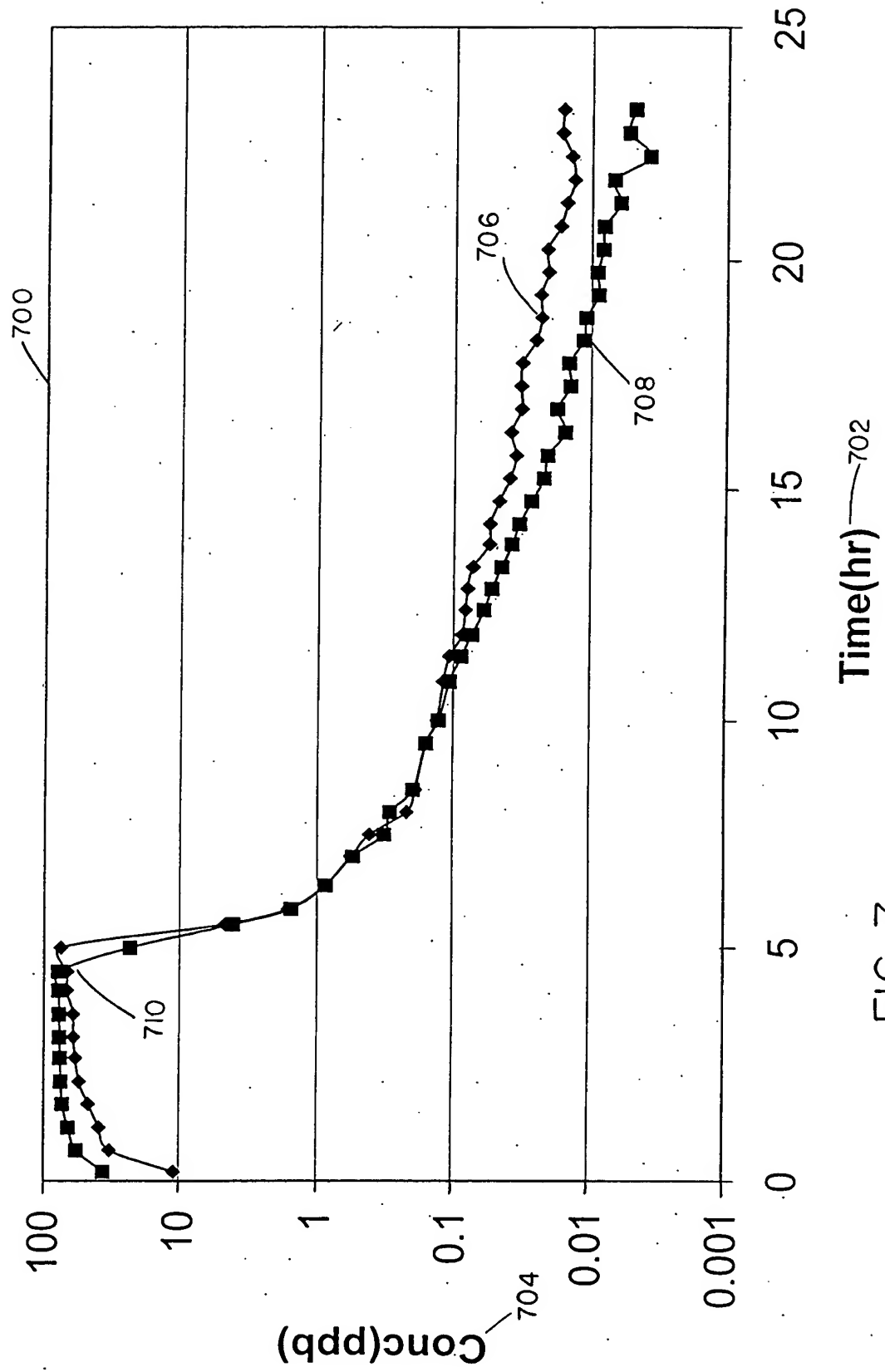


FIG. 7

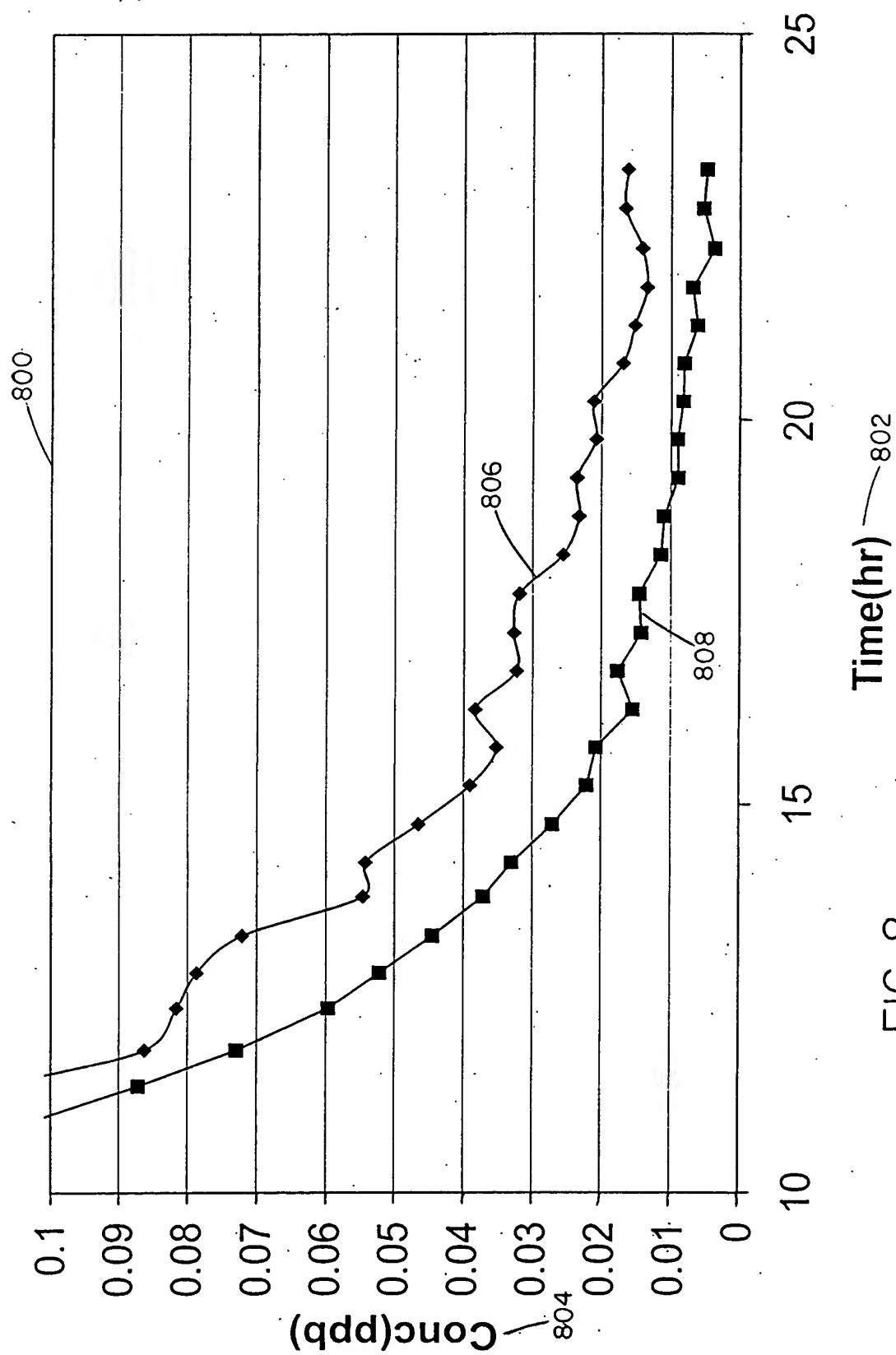


FIG. 8

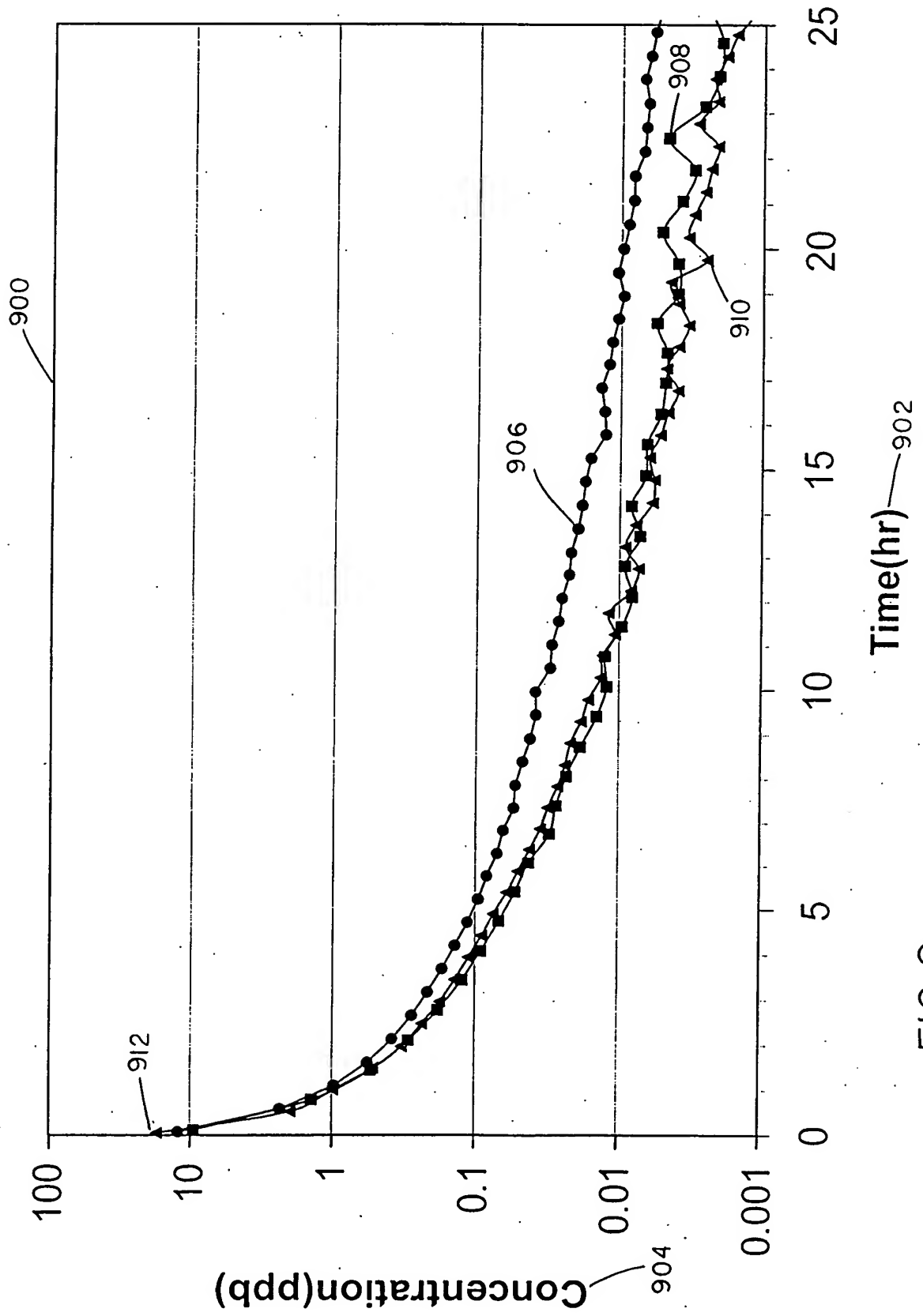


FIG. 9



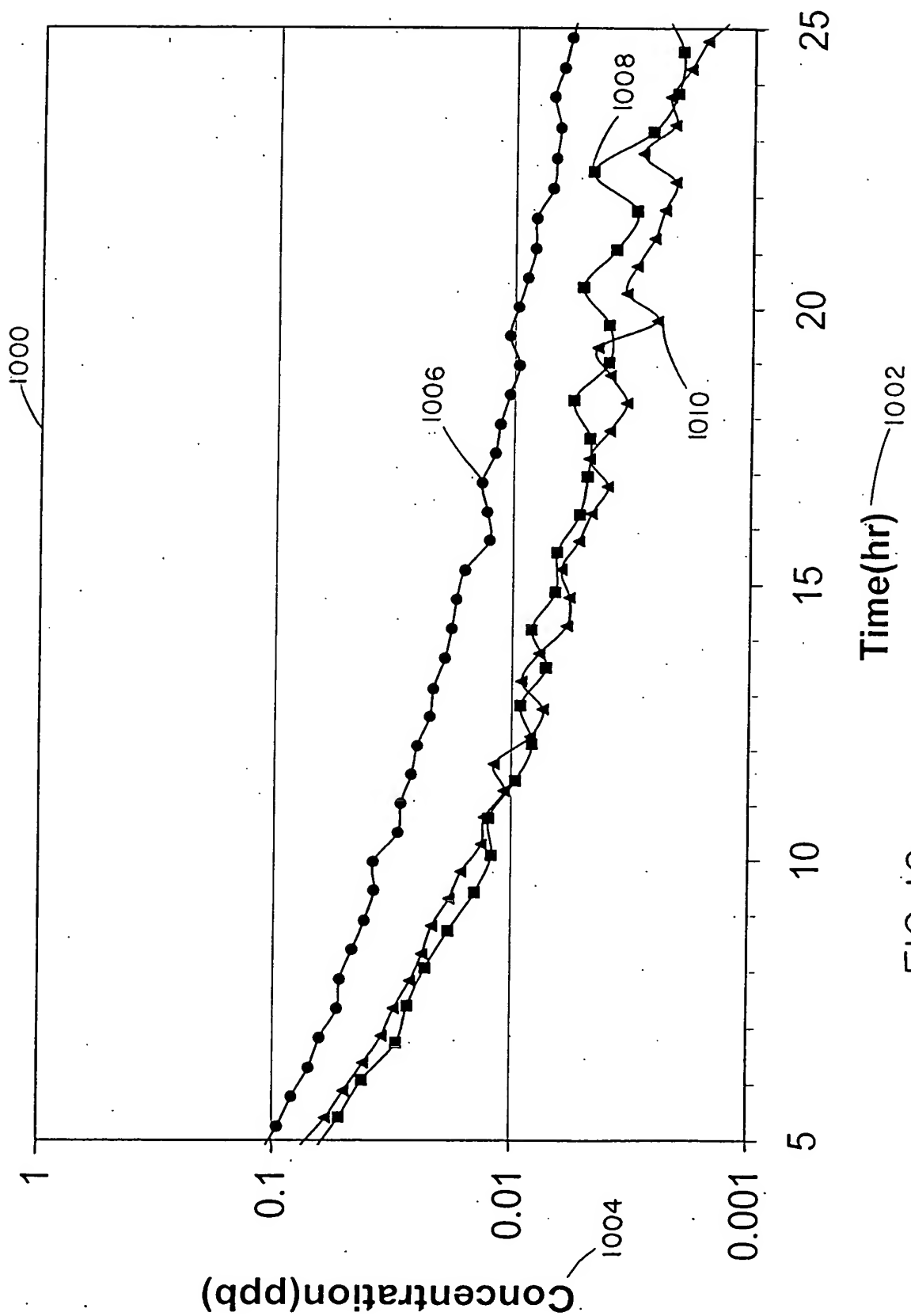


FIG. 10

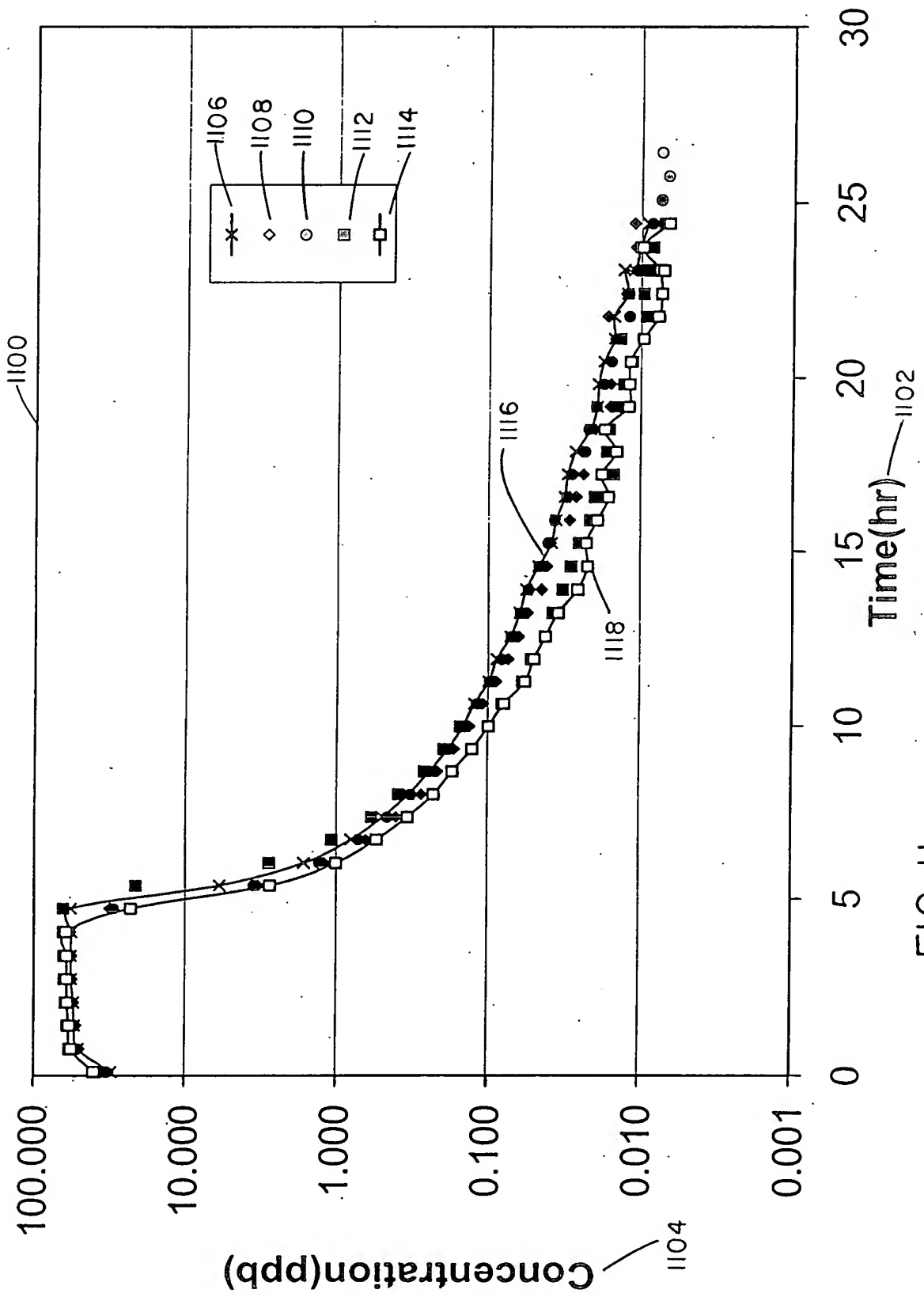


FIG. 11

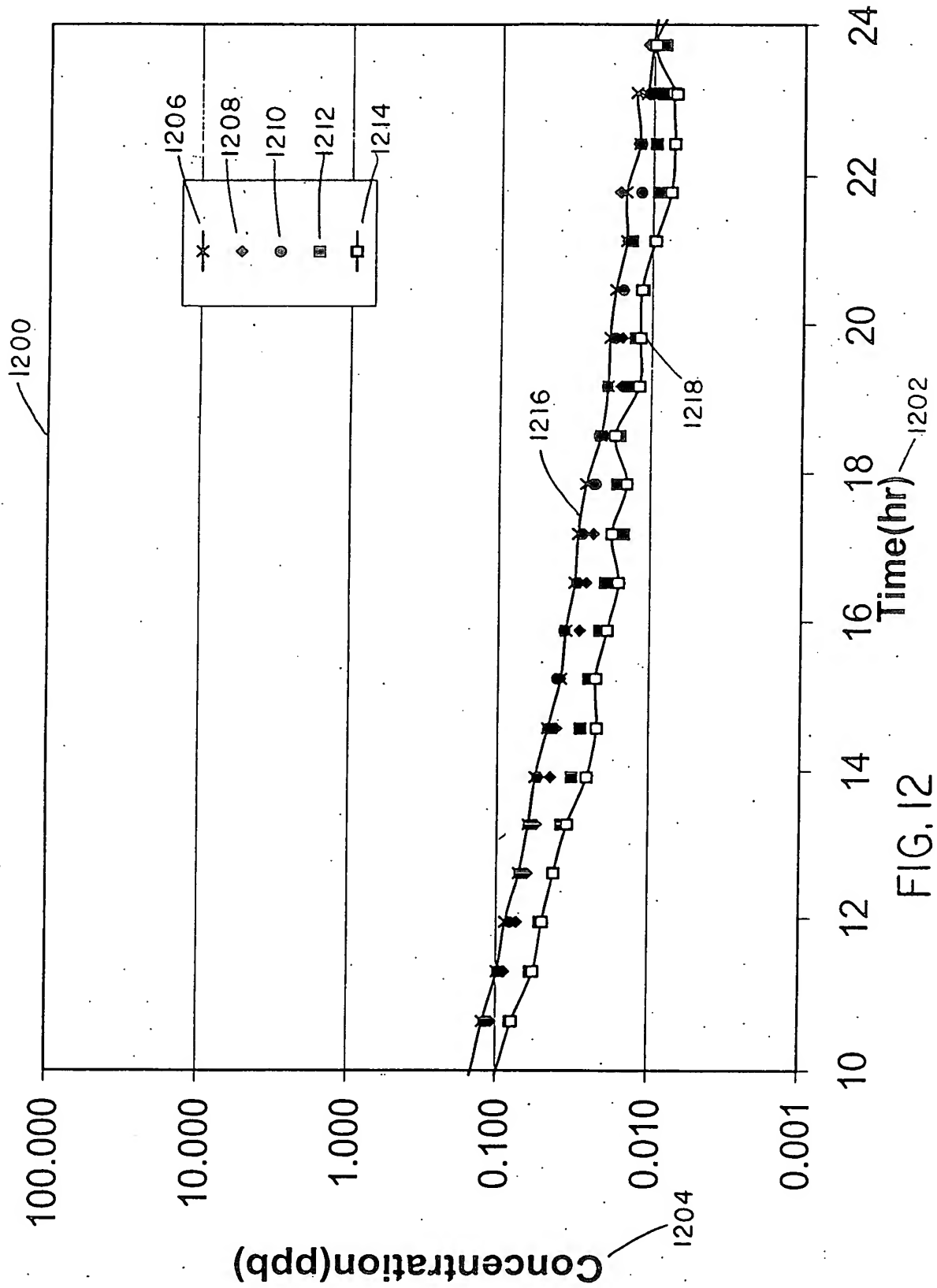


FIG. 12

Figure 13

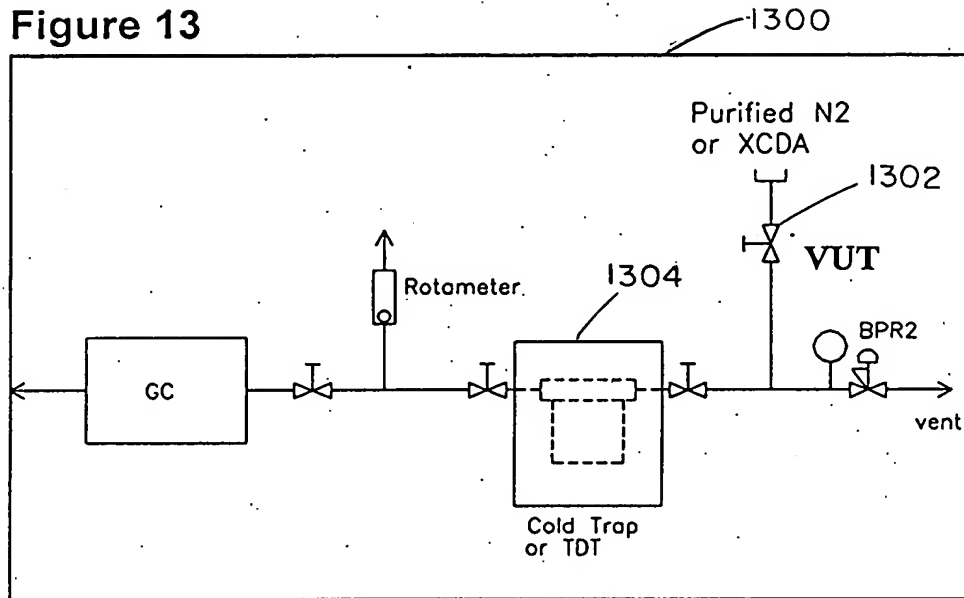


Figure 14

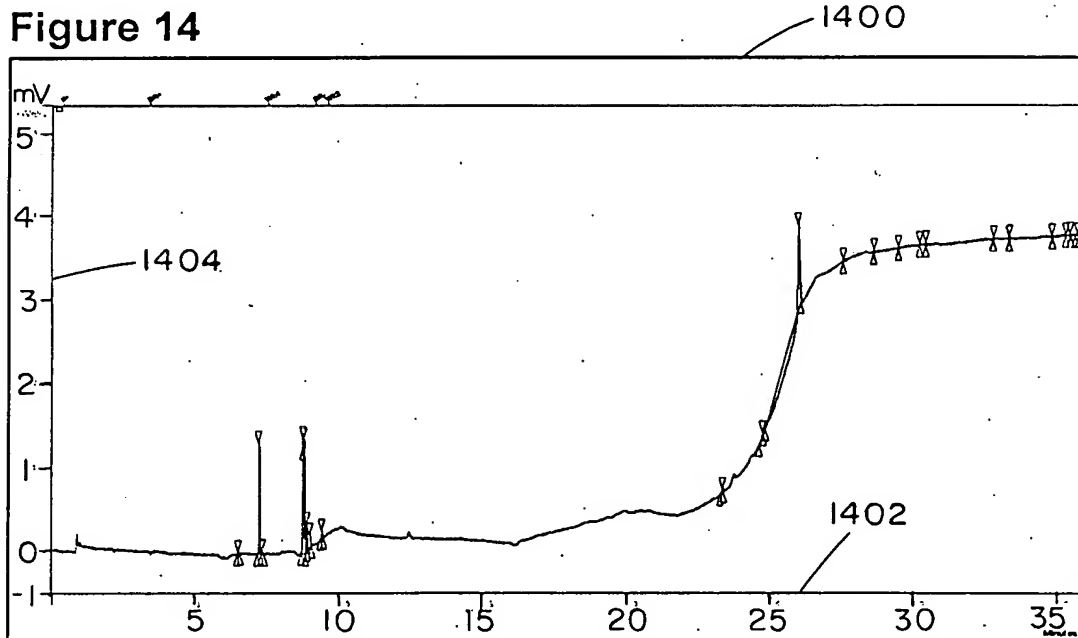
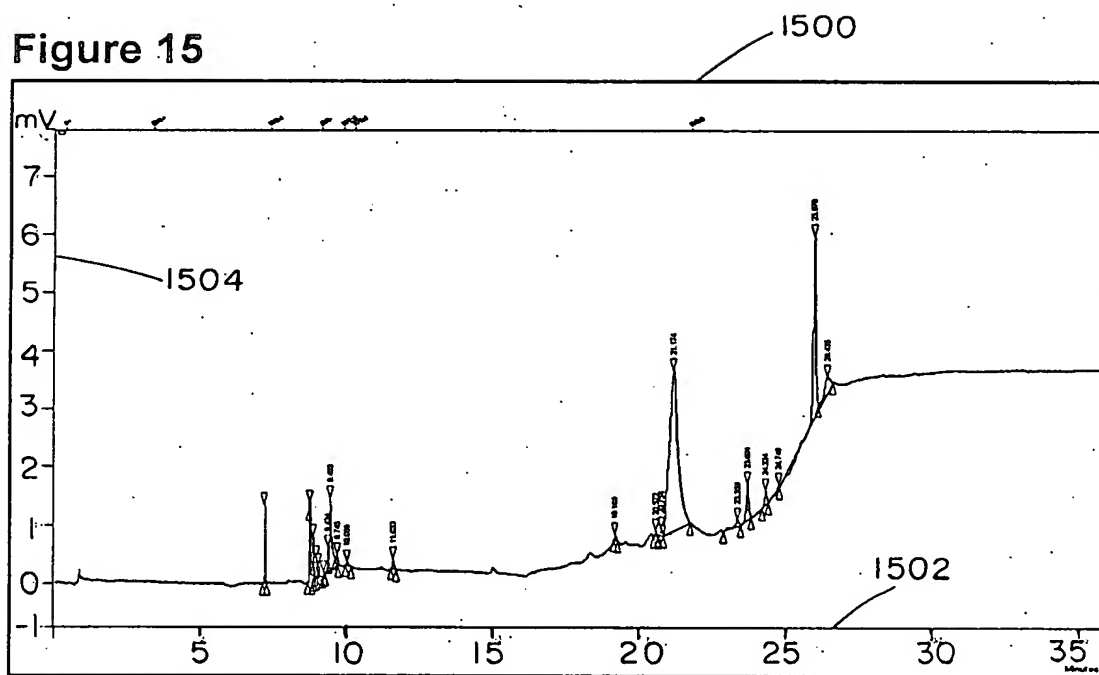


Figure 15



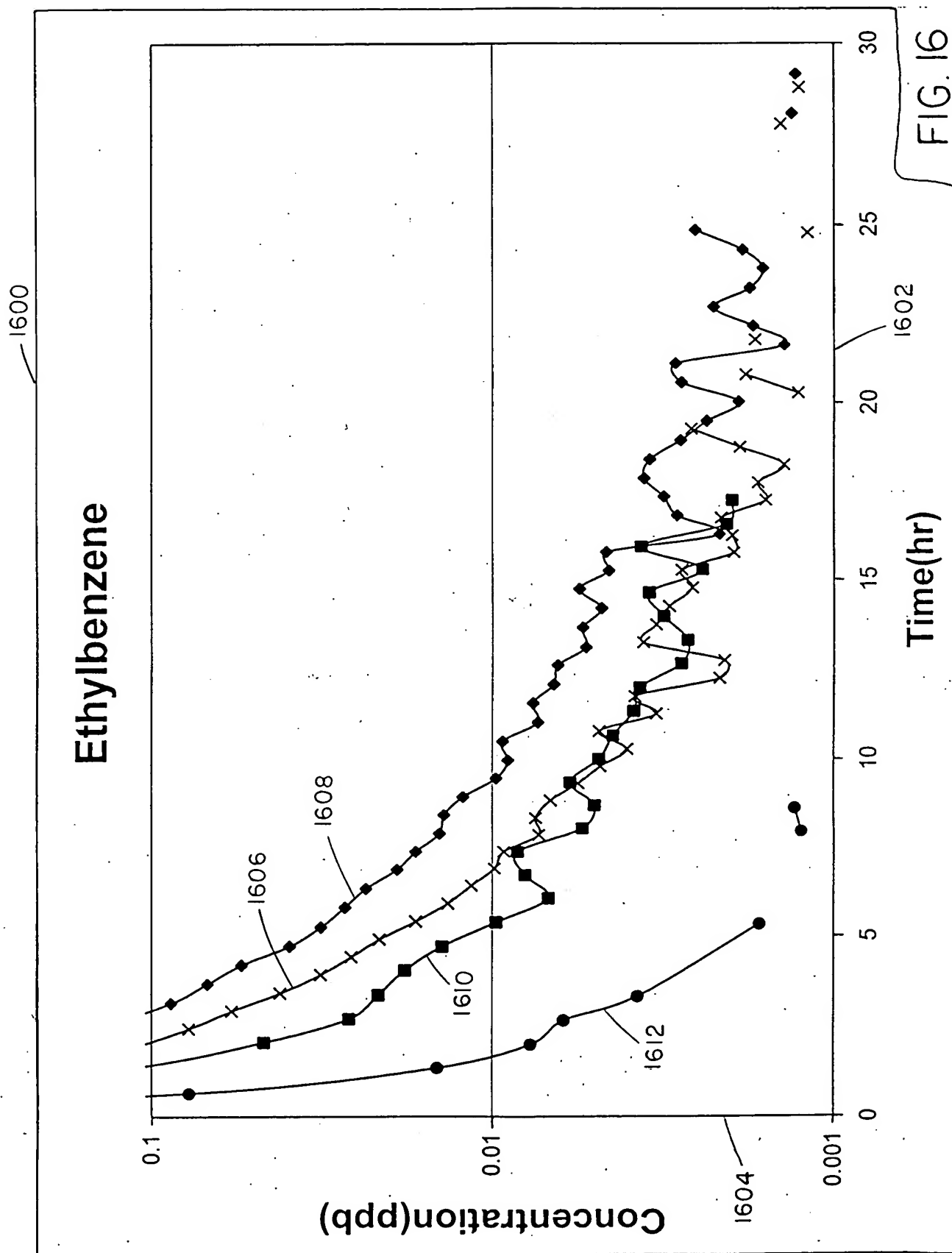
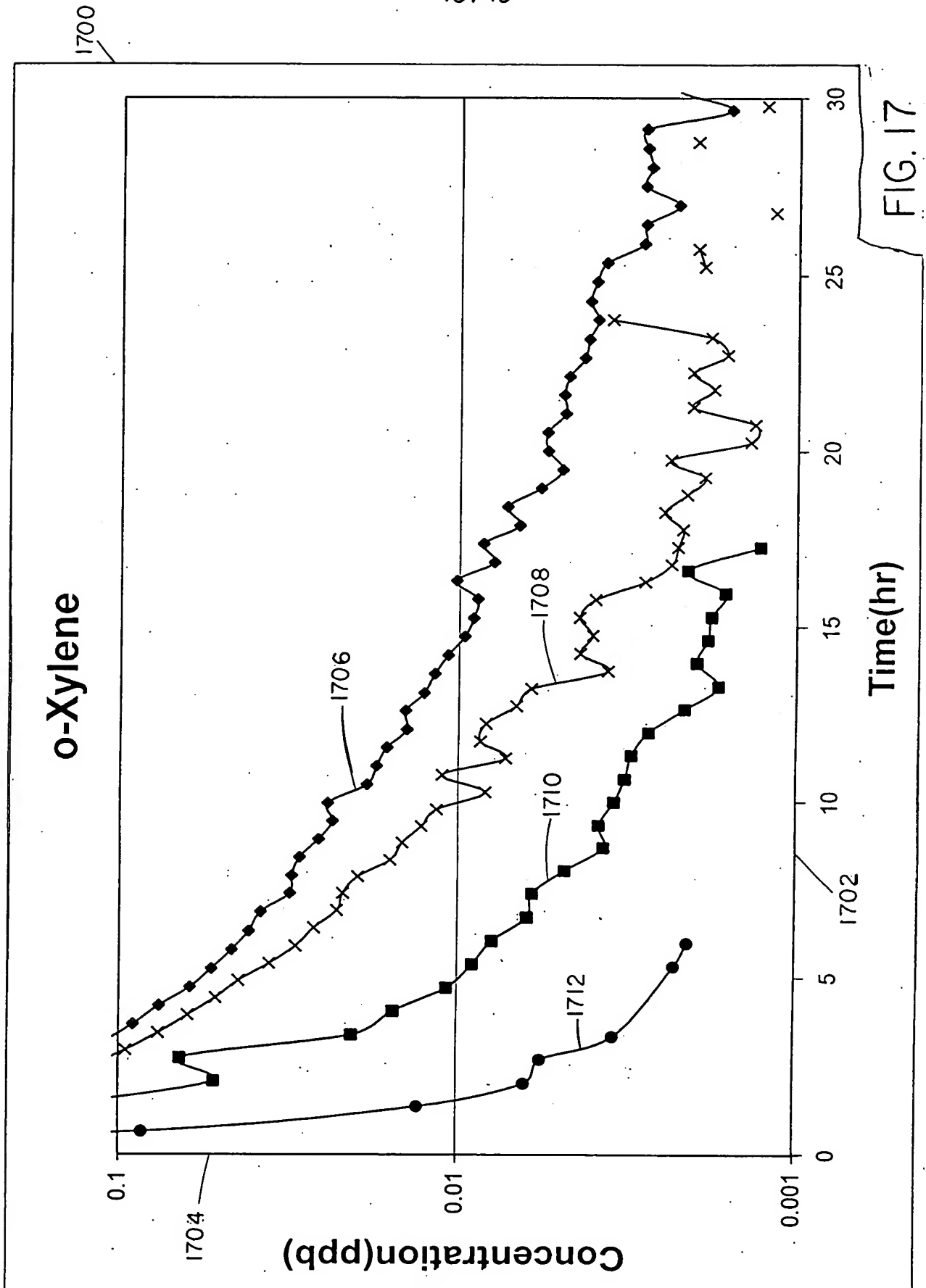
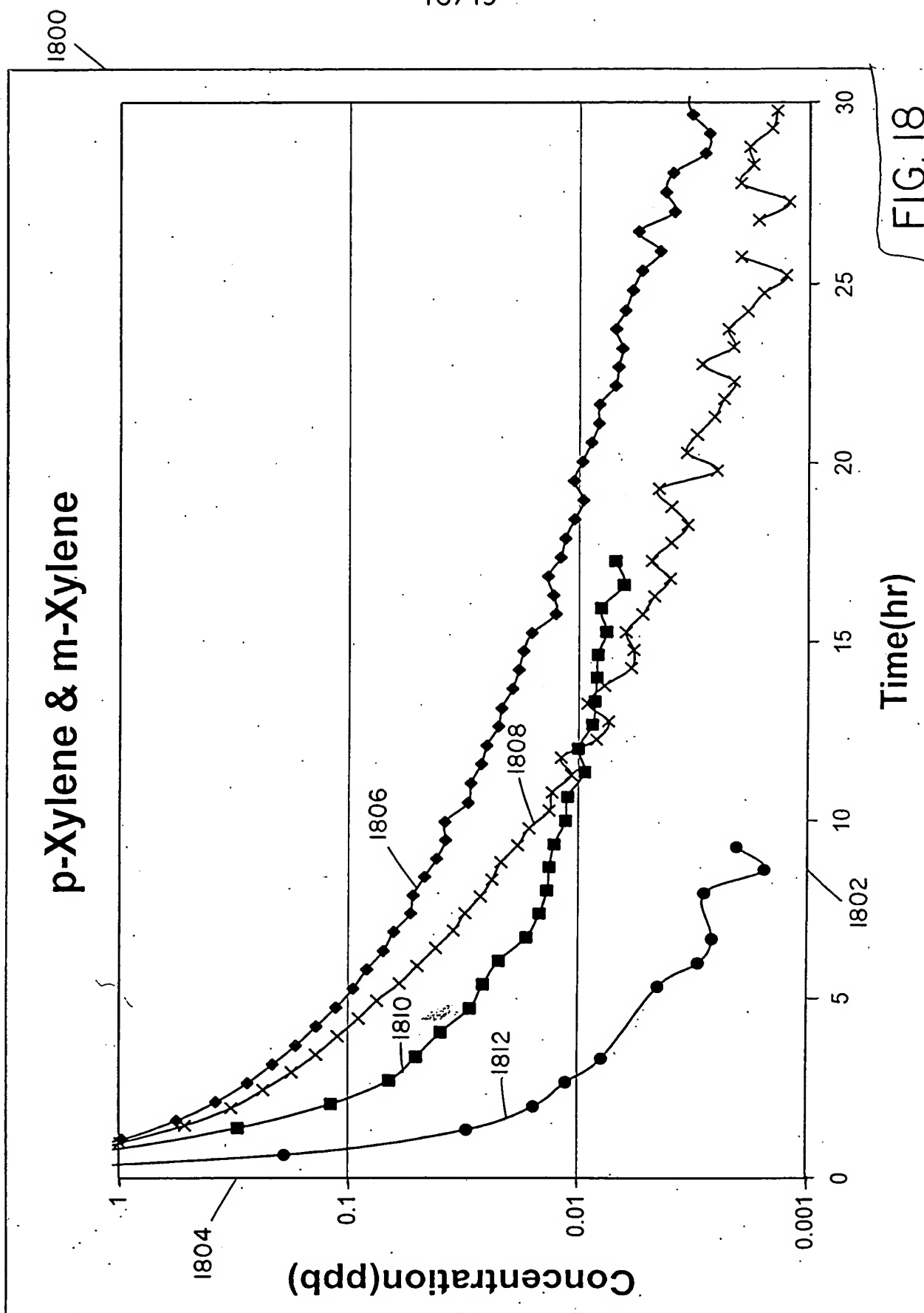


FIG. 16







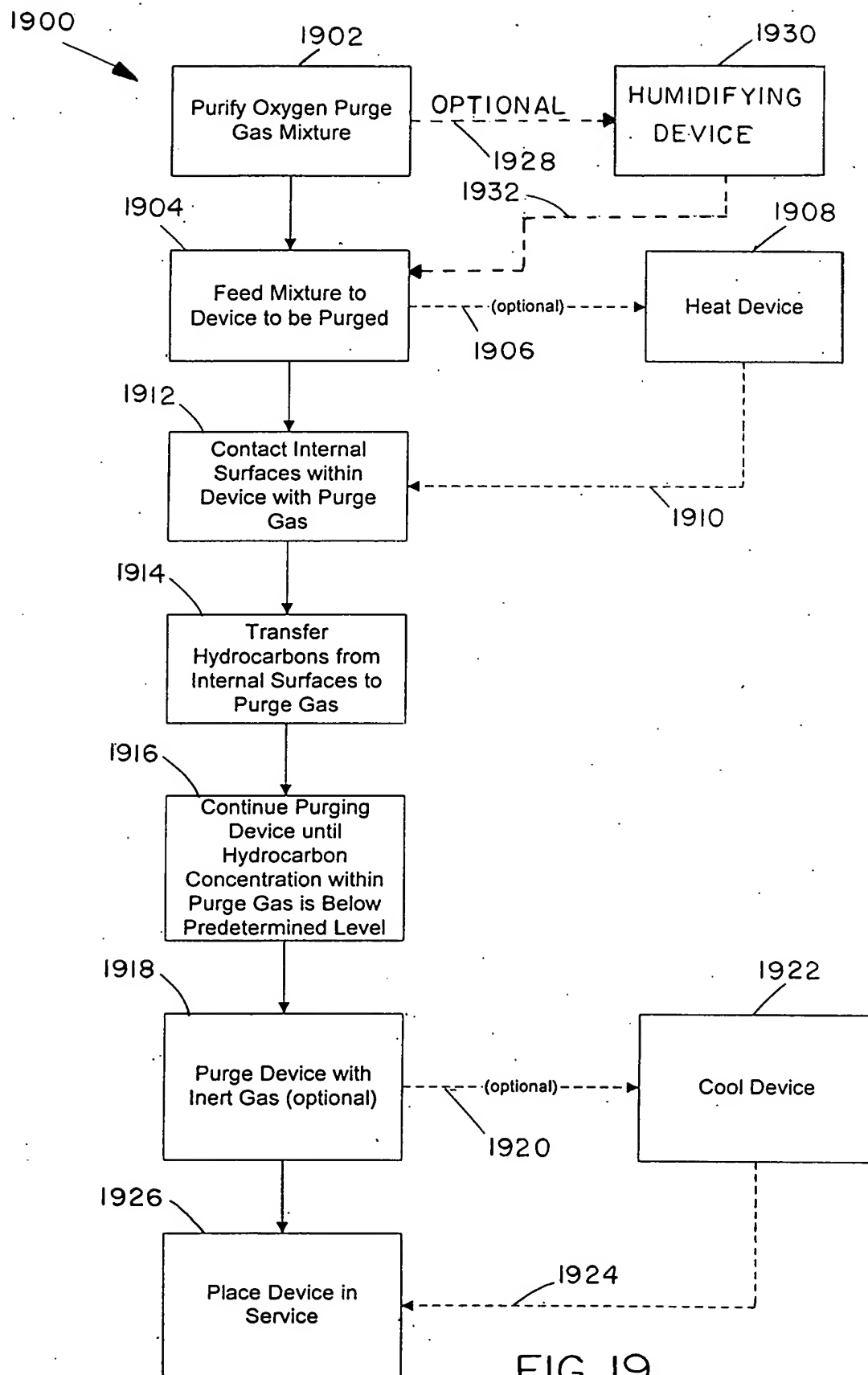


FIG. 19

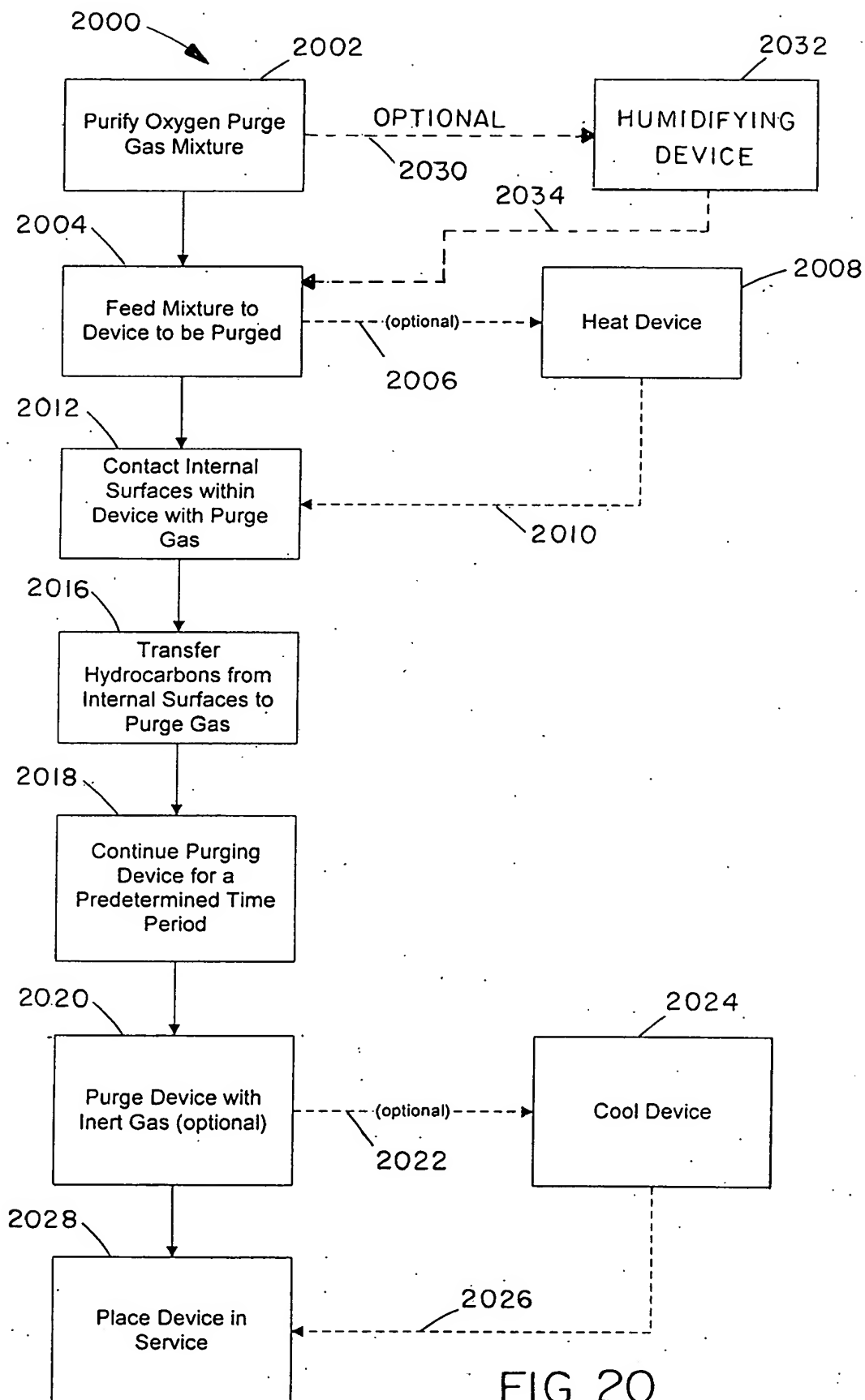


FIG. 20

